

CBBE DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:23 PAGE 2  
XCB8E0.P11 12-OCT-78 11:54

SEQ 0001

.REM \_

IDENTIFICATION

PRODUCT CODE: AC-F836E-MC  
PRODUCT NAME: CXCB8E0 CB11 DISTRIBUTE MOD  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITAL'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1973,1978 DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT

CBB IS A BKMOD THAT EXERCISES UP TO "N" CB11 DISTRIBUTOR MODULES HAVING CONTIGUOUS UNIVIRUS ADDRESSES. THE MAXIMUM VALUE IF "N" IS THE SAME AS THE MAXIMUM NO. OF DISTRIBUTORS MODULES ALLOWED FOR A SINGLE CB11. NON-CONTIGUOUS GROUPS OF DISTRIBUTOR MODULES MAY BE EXERCISED BY CONFIGURING THE CBB MODULE FOR EACH GROUP. THE MODULE SIMPLY TESTS THE ABILITY TO SET AND CLEAR ALL BITS IN ALL AVAILABLE DISTRIBUTE REGISTERS SELECTED FOR TEST. IF ANY BIT FAILS TO SET OR CLEAR PROPERLY THE ERROR IS REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS:

HARDWARE: A CB11 INTERFACE WITH AT LEAST ONE DISTRIBUTE MODULE.

STORAGE:: CBB REQUIRES:

1. DECIMAL WORDS: 126
2. OCTAL WORDS: 0176
3. OCTAL BYTES: 374

3. PASS DEFINITION

ONE PASS OF THE CBB MODULE RESULTS IN 100. ITERATIONS OF THE BASIC TEST SEQUENCE WHICH CLEARS AND SETS ALL DISTRIBUTOR REGISTERS SELECTED FOR TEST

4. EXECUTION TIME

CBB RUNNING ALONE ON A PDP 11/05 SYSTEM WITH ONE DISTRIBUTE MODULE SELECTED TAKES APPROXIMATELY 10 SECONDS.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVAOR: 0, VECTOR:0, BR1:0, BR2:0, DVECNT:1, SR1:0

REQUIRED PARAMETERS:

FOR EACH COPY OF CBB CONFIGURED THE  
USER MUST SPECIFY THE FOLLOWING PARAMETERS:

DEVADR: EQUAL TO THE FIRST ADDRESS IN A  
CONTIGUOUS GROUP  
SR1: NUMBER OF MODULES (SEE "OPERATION OPTIONS")

6. DEVICE OPTION SETUP:

NONE REQUIRED

7. MODULE OPERATION

TEST SEQUENCE.

- A. SET UP THE PASS COUNTER FOR 100. ITERATIONS
- B. GET THE CONTENTS OF SR1 TO FIND OUT HOW MANY REGISTERS TO TEST
- C. CLEAR A DISTRIBUTE REGISTER
- D. COUNT IT
- E. REPORT ANY ERROR
- F. GENERATE NEXT ADDRESS
- G. REPEAT B-F UNTIL ALL REGISTERS TESTED FOR ALL ZEROES
- H. REPEAT B-G BUT SET ALL REGISTERS TO ALL ONES AND TEST
- I. COUNT ONE ITERATION
- J. IF NOT 100. REPEAT B-I
- K. REPEAT END PAS, RESTART AT A.

8. OPERATION OPTIONS

- A. USER CAN MODIFY "ADDR" AND "SR1" TO SELECT ANY GROUP OF DISTRIBUTE MODULES

SR1: THE NUMBER (OCTAL) OF DISTRIBUTE MODULES TO BE TESTED (MUST BE CONTIGUOUS). THIS NUMBER MUST BE A STRAIGHT OCTAL NUMBER, NOT A BIT MAP AS IS NORMALLY USED IN DEVCNT. DEV\_CNT WAS NOT USED BECAUSE IT CAN HOLD A MAXIMUM OF 16 DEVICES AND CBB CAN RUN UP TO 256 DEVICES. IF SR1 IS LEFT AT ZERO OR OTHERWISE IMPROPERLY SET UP, THE SYSTEM WILL EVENTUALLY CRASH.

9. NON-STANDARD PRINTOUTS

NONE-ALL PRINTOUTS HAVE THE STANDARD DEC/X11 FORMATS.

FCB11 DISTRIBUTE MODULE - DEC/X11 EXERCISER MODULE

```

000000- BNNOD <CBBE> 6666,,100,,34
        MODULE 40200 5555,,5555,,34
        ; TITLE CBBE DEC/X11 SYSTEM EXERCISER MODULE
        ; DDXCOM VERSION 6 23-MAY-78
        ; LIST BIN
*****
000000- 041103 042502 040 BEGIN:
000000- 000000 000000 000000 MODNAME: ASCII /CBBE / ;MODULE NAME
000000- 000000 000000 000000 XBUF: BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000000- 000000 000000 000000 ADDR: +0 ;1ST DEVICE ADDR
000000- 000000 000000 000000 VECTOR: +0 ;1ST DEVICE VECTOR.
000000- 000000 000000 000000 BR1: .BYTE PRTY+0 ;1ST BR LEVEL.
000000- 000000 000000 000000 BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
000000- 000001 000000 000000 DIVID1: +0 ;DEVICE INDICATOR 1.
000000- 000000 000000 000000 SRI: OPEN ;SWITCH REGISTER 1.
000000- 000000 000000 000000 SR41: OPEN ;SWITCH REGISTER 3.
000000- 000000 000000 000000 SR42: OPEN ;SWITCH REGISTER 4.
000000- 000000 000000 000000 SR43: OPEN ;SWITCH REGISTER 5.
000000- 000000 000000 000000 SR44: OPEN ;SWITCH REGISTER 6.
*****
000026- 040920- STAT: 40020 STATUS WORD.
000032- 000224- INIT: START ;MODULE STACK ADDR.
000032- 000224- SPPOINT: MODSP ;MODULE STACK POINTER.
000032- 000224- PAGINT: 0 ;PASS COUNT.
000032- 000224- ICONT: 00- ;# OF ITERATIONS PER PASS=100.
000045- 000144- SOFCNT: 0 ;LOC TO COUNT TOTAL SOFT ERRORS.
000045- 000000- SOFPAS: 0 ;LOC TO COUNT TOTAL HARD ERRORS.
000045- 000000- SOFPAS: 0 ;LOC TO COUNT SOFT ERRORS PER PASS.
000050- 000000- HRDCNT: 0 ;LOC TO COUNT HARD ERRORS PER PASS.
000052- 000000- SVR0: 0 ;# OF SW ERRORS ACCUMULATED.
000056- 000000- RANNUM: 0 ;HOLDS RANDOM N WHEN RAND MACRO IS CALLED.
000056- 000000- CONFIG: 0 ;RESERVED FOR MONITOR USE.
000056- 000000- RES1: 0 ;RESERVED FOR MONITOR USE.
000060- 000000- RES2: 0 ;RESERVED FOR MONITOR USE.
000062- 000000- SVR0: OPEN ;LOC TO SAVE R0.
000064- 000000- SVR1: OPEN ;LOC TO SAVE R1.
000065- 000000- SVR2: OPEN ;LOC TO SAVE R2.
000066- 000000- SVR3: OPEN ;LOC TO SAVE R3.
000072- 000000- SVR4: OPEN ;LOC TO SAVE R4.
000076- 000000- SVR5: OPEN ;LOC TO SAVE R5.
000076- 000000- SVR6: OPEN ;LOC TO SAVE R6.
000102- 000000- CSRA: OPEN ;ADDR OF CURRENT CSR.
000102- 000000- SBADR: OPEN ;ADDR OF GOOD DATA, OR
000104- 000000- MSRA: OPEN ;ADDR OF BAD DATA, OR
000104- 000000- MSTAT: OPEN ;STATUS REG CONTENTS.
000105- 000000- ERRtyp: 0 ;TYPE OF ERROR.
000105- 000000- ASB: OPEN ;EXPECTED DATA.
000119- 000000- AWAS: OPEN ;ACTUAL DATA.
000119- 000236- RSTART: RESTRT ;RESTART ADDRESS AFTER END OF PASS.
000119- 000236- WDFD: OPEN ;# WORDS FROM MEM/ITERATION.
000115- 000000- WDFD: OPEN ;# WORDS FROM MEMORY PER ITERATION.
000120- 000000- INTR: OPEN ;# OF INTERRUPTS PER ITERATION.
*****
```

```

000122* 000034 IDNUM: 34 ;MODULE IDENTIFICATION NUMBER=34
000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
        .NLIST
        .WORD 0
        .LIST
        .ENDR
000224* MODSP: ;*****
223 000224* 012767 000002 177664 START: NOV #2,WDFR ;2 WORDS FROM MEM/ITERATION
225 000232* 016700 177550 RESTRT: NOV ADDR,R0 ;GET THE 1ST REG. ADDRESS
226 000232* 016702 177554 NOV SRI,R2 ;GET THE DISTR. MODULE COUNT
227 000244* 000230- ASL R2 ;MAKE IT A REG. COUNT
228 000244* 000230- 1S: BNE ,R0) ;CLEAR THE REGISTER
229 000246* 001024- 4S: DEC R2 ;COUNT ONE REG. DON'T CLEAR ALL BITS
230 000250* 005302- 4S: BEQ 3S ;BR IF ALL REGS CLEARED
231 000252* 001402- TST ,R0)+ ;GENERATE NXT ADDRESS
232 000254* 005720- BR 1S ;GO CLEAR THE NEXT ONE
233 000256* 000772- 3S: NOV ADDR,R0 ;GET THE 1ST REG. ADDRESS
234 000256* 000772- NOV SRI,R2 ;GET THE DISTR. MODULE COUNT
235 000270* 006302 177526 SRL R2 ;MAKE IT A REG. COUNT
236 000272* 005110- 8S: COM ,R0) ;SET ALL REGS COUNT
237 000272* 005110- 8S: CMP #17777,(R0) ;DID ALL BITS SET ?
238 000274* 022710 177777 8S: BNE 6S ;BR IF NOT
239 000300* 001022- 7S: DEC R2 ;COUNT ONE REG.
240 000302* 005302- 7S: BEQ 2S ;BR IF ALL REGS SET
241 000302* 001402- TST ,R0)+ ;GENERATE THE NXT ADDRESS
242 000310* 000770- BR 6S ;GO DO ANOTHER ONE
243 000312* 104413 000000- 2S: ENDITS,BEGIN ;SIGNAL END OF ITERATION.
244 000312* 104413 000000- ENDIT,BEGIN ;MONITOR SHALL TEST END OF PASS.
245 000316* 000745- 5S: BR RESTRT ;BR TIL 100. TIMES THROUGH
246 000324* 011067 177554 5S: NOV R0,CSRA ;SAVE THE ADDRESS OF THE REG.
247 000324* 011067 177554 NOV R0,ACSR ;SAVE THE CURRENTS
248 000324* 011067 177554 NOV R0,ACSP ;SAVE THE CURRENTS
249 000330* 012767 000025 177550 NOV R0,CSRP ;SAVE THE REG.
250 000330* 012767 000025 177550 HRSRS,BEGIN,NULL ;FAILED TO CLEAR ALL BITS
251 000336* 104405 000000* 000000- HRSRS,BEGIN,NULL ;FAILED TO CLEAR ALL BITS
252 000344* 000741- 6S: BR 4S ;GO TRY THE NEXT GUY
253 000346* 011067 177526 6S: NOV R0,CSRA ;SAVE THE ADDRESS OF THE REG.
254 000346* 011067 177526 NOV R0,ACSR ;SAVE THE CURRENTS
255 000346* 011067 177526 NOV R0,ACSP ;SAVE THE CURRENTS
256 000356* 012767 000025 177522 HRSRS,BEGIN,NULL ;BIT STUCK IN REG.
257 000364* 104405 000000* 000000- HRSRS,BEGIN,NULL ;FAILED TO SET ALL BITS
258 000364* 104405 000000* 000000- HRSRS,BEGIN,NULL ;FAILED TO SET ALL BITS
259 000372* 000743- 7S: BR 7S ;GO TRY THE NEXT GUY
260 000372* 000743- .END
261 000001
*****
```

ACSR	000102R	205#	249*	256*
ADDR	000100R	223#	225	234
ADDR22=	001000R	223#		
ASB	000105R	209#		
ASTAT	000104R	207#		
AWAS	000101R	210#		
BEGIN	000000R	168#	245	252 259
BIT0	= 000001	223#		
BIT1	= 000002	223#		
BIT11	= 002000	223#		
BIT12	= 010000	223#		
BIT13	= 020000	223#		
BIT14	= 040000	223#		
BIT15	= 100000	223#		
BIT2	= 000004	223#		
BIT3	= 000008	223#		
BIT4	= 000020	223#		
BITS	= 000040	223#		
BIT7	= 000100	223#		
BIT8	= 000200	223#		
BIT9	= 000400	223#		
BREAKS=	104407	223#		
BR1	000012R	174#		
BR2	000013R	174#		
BTODS	= 104421	223#		
CDATAS	= 104412	223#		
CONFIG	000058R	193#		
CSR	000010R	203#	248*	255*
DACKS=	104404	223#		
DATERS=	104404	223#		
DVIDL	= 000014R	175#		
ENDITS=	104413	223#	245	
ENDS=	= 104410	223#		
ERRTYP	000106R	108#	250*	257*
EXITS	= 104400	223#		
EWXFS=	104411	223#		
GWBUFF=	104414	223#		
HRDENT	000044R	188#		
HRDERS=	104405	223#		
HRDPAS	000050R	190#		
ICONT	000036R	185#		
ICOUNT	000014R	186#		
IDUM	000100R	205#		
INW1	000030R	191#		
INW2	= 000120R	214#		
MAP22S=	104416	223#		
MODNAME	000000R	169#		
MODSP	000224R	183	221*	
MSG02	= 104403	223#		
MSG03	= 104402	223#		
MSG04	= 104401	223#		
NULP	= 000000	252	259	
OPEN	= 000000	170	176	200 201 202 203
		205	207	209 210 212 213 214 216 223# 199 200 201 202 203

OTOTS=	104420	223#		
PASCNT	000034R	184#		
PIROS=	000004	223#		
POPSP	= 005726	223#		
POPSP2=	005726	223#		
PR1	= 000000	223#		
PRTY0	= 000000	223#		
PRTY1	= 000040	223#		
PRTY2	= 000100	223#		
PRTY3	= 000140	223#		
PRTY4	= 000200	223#		
PRTY5	= 000240	223#		
PRTY6	= 000340	223#		
PRTY7	= 000340	223#		
PS	= 177776	223#		
PSW	= 177776	223#		
PUSH1	= 005746	223#		
PUSH2	= 024646	223#		
RANDS	= 004414R	223#		
RANMIN	000100R	223#		
RDSRT	000232R	211#	225*	247
RFS1	000055R	194#		
RFS2	= 000060R	165#		
RSTART	000112R	211#		
SBADR	000102R	204#		
SOPCNT	000042R	187#		
SOPFBS=	000046R	180#		
SOPINT	= 000032R	183#		
SPSIZ=	= 000040	1#	216	235
SR1	= 00016R	176#	226	
SR2	= 00020R	177#		
SR3	= 00022R	178#		
SR4	= 00024R	179#		
STRART	000002R	182#	224*	
STAT	= 00026R	181#		
SVR0	= 000062R	196#		
SVR1	= 000064R	197#		
SVR2	= 000066R	198#		
SVR3	= 000070R	199#		
SVR4	= 000072R	200#		
SVR5	= 000074R	201#		
SVR6	= 000076R	202#		
SVSCNT	= 000052R	201#		
TRPDID=	000022	223#		
VECTOR	= 00010R	172#		
WASADR	= 000104R	206#		
WDFR	= 000110R	213#		
WDTG	= 000112R	214#		
XFLAG	= 000005R	170#		

\* ABS- 000000 000  
000374 001

CB8F DEC/X11 SYSTEM EXERCISE MODULE MACY11 30A(1052) 12-OCT-78 16:23 PAGE 10  
XCB8E0.P11 12-OCT-78 11:54 CROSS REFERENCE TABLE -- USER SYMBOLS  
ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0  
XCB8E0 XCB8E0/SOL/CPY;SYM=DDXCOM,XCB8E0  
RUN-TIME: 11.2 SECONDS  
RUN-TIME RATIO: 8/2=3.3  
CORE USED: 7K (15 PAGES)

SEQ 0008